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Electronic Patent Application Submission
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EFS ID: 62795

Application ID: 10810035



Title of Invention: NON-VOLATILE TRANSISTOR
MEMORY ARRAY
INCORPORATING READ-ONLY
ELEMENTS WITH SINGLE MASK
SET

First Named Inventor: Albert Weiner

Domestic/Foreign Application: Domestic Application

Filing Date: 2004-03-26

Effective Receipt Date: 2004-06-17

Submission Type: Information Disclosure
Statement

Filing Type:

Confirmation number: 8458


Attorney Docket Number: ATM-291

Total Fees Authorized:

Digital Certificate Holder: cn=Thomas Schneck,ou=Registered Attorneys,ou=Patent and Trademark
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Certificate Message Digest: 212c55b29853207423034907be6e6af50b722305

**TRANSMITTAL**

Electronic Version v1.1
Stylesheet Version v1.1.0


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<p>Application Number: 10/810035 </p> <p>Date: 2004-03-26</p> <p>First Named Applicant: Mr. Albert S. Weiner</p> <p>Confirmation Number: 8458</p> <p>Attorney Docket Number: ATM-291</p>										
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Mr. Thomas Schneck Registered Number: 24518</td><td>/thomas schneck/</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Mr. Thomas Schneck Registered Number: 24518	/thomas schneck/	Attorney		
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<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>ATM291.IDS-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	ATM291.IDS-usidst.xml		us-ids.dtd		us-ids.xsl
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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